

Lab Assignment no 1

Course: Computer Architecture

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BSCS (R) 5th semester

Session 2023-27

Question :

What is a 2-to-4 decoder? Draw its truth table and logic diagram.

Instructions:

The assignment must be completed **in detailed form**. Please write full explanations of question and **include the required diagrams**.

You may submit the assignment **either handwritten or in printed form**, but the work must be **original and not copied**.

Ensure neat and clear work before submission.

Thank you.